



SRI VENKATESWARA COLLEGE OF ENGINEERING (AUTONOMOUS)

Karakambadi Road, Tirupati-517507

R25

EXAMINATION BRANCH

M.Tech I Semester (R25) Regular Examinations Feb-2026 TIME TABLE

Exam Timings: **10.00 AM TO 1.00 PM**

Date/Day	VLSI Design (VLSI D)	Computer Science and Engineering (CSE)
02-02-2026 (Monday)	CMOS Analog IC Design EC25DPC101	Advanced Data Structures and Algorithms CS25DPC101
04-02-2026 (Wednesday)	CMOS Digital IC Design EC25DPC102	Distributed Operating Systems CS25DPC102
06-02-2026 (Friday)	CAD for VLSI EC25DPE101	Advanced Computer Architecture CS25DPE101
	Hardware for Deep Learning EC25DPE102	Cloud Computing CS25DPE102
	Microchip Fabrication Techniques EC25DPE103	Applied Machine Learning CS25DPE103
	Modeling and Synthesis with Verilog HDL EC25DPE104	Object Oriented Software Engineering CS25DPE104
09-02-2026 (Monday)	ASIC Design EC25DPE105	Natural Language Processing CS25DPE105
	FPGA Architectures and Applications EC25DPE106	Smart Sensors and IOT CS25DPE106
	MOS Device Modelling EC25DPE107	Computing for Data Analytics CS25DPE107
	Organic Electronics EC25DPE108	Information Security CS25DPE108
11-02-2026 (Wednesday)	Research Methodology and IPR EC25DRM101	

Y. A. Sivaraj
Controller of Examinations
Date: 20-01-2026

S. Jeyaraj
Principal